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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/662,809		09/16/2003	Seiji Nagai	T36-159069M/KOH	1114	
21254	7590	03/27/2006		EXAMINER		
		ECTUAL PROF	RAO, G NAGESH			
SUITE 200	8321 OLD COURTHOUSE ROAD SUITE 200				PAPER NUMBER	
VIENNA, V	'A 2218	32-3817	1722			

DATE MAILED: 03/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Δ	Application No.		Applicant(s)	_				
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Office Action Summa		10/662,809		NAGAI ET AL.					
Onice Action Summary		xaminer		Art Unit					
The MAILING DATE of this co	1	3. Nagesh Rao	r shoot with the	1722	_				
Period for Reply	пппипсаноп арреа	ns on the cove	r sneet with the t	onespondence address					
A SHORTENED STATUTORY PER WHICHEVER IS LONGER, FROM 7 Extensions of time may be available under the prafter SIX (6) MONTHS from the mailing date of the strength of the stren	THE MAILING DAT rovisions of 37 CFR 1.136(a his communication. timum statutory period will a for reply will, by statute, camonths after the mailing da	E OF THIS Co a). In no event, how apply and will expire use the application	OMMUNICATION vever, may a reply be tin s SIX (6) MONTHS from to become ABANDONE	N. nely filed the mailing date of this communication. ED (35 U.S.C. § 133).					
Status									
1) Responsive to communication	(s) filed on <u>11 July</u>	<u>2005</u> .							
2a) This action is FINAL .									
3) Since this application is in con									
closed in accordance with the	practice under Ex j	pane Quayie,	1935 C.D. 11, 4:	53 O.G. 213.					
Disposition of Claims									
4) Claim(s) 1-18 is/are pending in									
	4a) Of the above claim(s) is/are withdrawn from consideration.								
5) Claim(s) is/are allowed									
6)⊠ Claim(s) <u>1-18</u> is/are rejected. 7)□ Claim(s) is/are objected	d to								
8) Claim(s) are subject to		election require	ement.						
				,					
Application Papers									
9) The specification is objected to		tad or b\□ ak	signated to by the	Evaminer					
10) The drawing(s) filed on Applicant may not request that a									
Replacement drawing sheet(s) in									
11) The oath or declaration is obje									
Priority under 35 U.S.C. § 119									
12)⊠ Acknowledgment is made of a a)⊠ All b)□ Some * c)□ Non		riority under 3	5 U.S.C. § 119(a	a)-(d) or (f).					
,	1.⊠ Certified copies of the priority documents have been received.								
2. Certified copies of the p	2. Certified copies of the priority documents have been received in Application No								
3. Copies of the certified of				ed in this National Stage					
application from the Inte	•								
* See the attached detailed Offic	e action for a list of	the certified o	opies not receive	ed.					
Attachment(s)									
1) Notice of References Cited (PTO-892)		4)	Interview Summar						
 2) Notice of Draftsperson's Patent Drawing R 3) Information Disclosure Statement(s) (PTO- 	•	5) [Pate Patent Application (PTO-152)					
Paper No(s)/Mail Date		6) [_	Other:						

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Allowable Subject Matter

The indicated allowability of claim 4 is withdrawn in view of the newly discovered reference(s) to Tischler (US PG Pub 2002/0028314). Rejections based on the newly cited reference(s) follow.

Claim Rejections - 35 USC § 112

2) Claims 1-2 and 8-9 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Applicant claims the a method utilizing halide vapor phase epitaxy deposition, however upon review of the specification, the HVPE process utilizes HCl gas etchant in carrying out the claimed "halide" process. The HCl is not a halide but a hydride agent, and further proof is substantiated in the art rejection below utilizing the Tischler (PG Pub 2002/0028314) reference as evidence that HCl when used encompasses a hydride vapor phase epitaxy method.

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Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3) Claims 1-18 are rejected under 35 U.S.C. 102(e) as anticipated by Tischler (US Pg Pub 2002/0028314).

Examiner has reviewed applicant's arguments and after consideration is withdrawing the initial rejection over the prior art of Shibata 610. However examiner is putting forth Tischler 314 since it appears to read on applicant's teachings. Examiner is also withdrawing the allowability of claim 4 based on discovery of the Tischler 314 reference.

Tischler 314 pertains to a process for producing Gallium-Nitride (GaN) semiconductor substrates via a variety of various methods in examples disclosed in the specification furthermore note that Tischler 314 refers to the GaN layer as a metal nitride (M*N) however that is understood by the examiner to be a synonym for a Group III-V nitride material (See Sections 0002-0013). Tischler 314 teaches a

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sacrificial silicon substrate having been etched away via HCl while the substrate/M*N structure is preferably maintained (See Sections 0014-0019).

The M*N layer for example GaN may be deposited directly on the surface of the crystalline or non-crystalline substrate, or alternatively it may be deposited on an upper most surface of one or more intermediate layers which in turn are deposited on the crystalline substrate. The one or more intermediate layers may serve as a buffer layer to enhance the crystallinity of the M*N layer, as a template for the subsequent M*N growth thereon, or the intermediate layer(s) may serve as protective layer(s) or as an etch stop to prevent the etchant for the sacrificial substrate from etching into the M*N material (See Section 0020-0021) but thereby denoting that the etch could occur from the rear portion of the sacrificial substrate since the intermediate buffer layers are followed by the substrate before the top layer of M*N is completely processed. As well the M*N layer could contain more aluminum in the Group III nitride compound in the event the layer is decidedly an AlN layer or a AlGaN or AlGaInN layer (See Sections 0092-0093).

The growth of the M*N layer material may be carried out in a HVPE reactor whereby although denoted as a hydride vapor phase epitaxy reactor, examiner qualifies this as an equivalent and capable of handling applicant's claim of a halide vapor phase epitaxy. Upon reviewing applicant's specification, examiner noted the

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reference of HVPE as being a halide based reactor due to the use of HCl (applicant's specification) as a gaseous etchant with respect to the specification language on page 10 Lines 9-20. Although Tischler 314 denotes HVPE differently, it does disclose the use of HCl as the gaseous etchant for creating the free standing M*N semiconductor substrate.

Furthermore Tischler 314 teaches processing parameters for the HVPE method have temperatures growth for a GaN layer be between 1000-1200°C and the desired thickness range be between 1-1000 microns but preferably at 100-300 microns thus reading on claimed thickness and temperature variations as claimed by applicant (See Section 0050 and 0025) as well the ability to grow more than one layer of M*N materials as suggested by the language of section 0051.

Finally Tischler 314 teaches an ability to prevent lattice mismatch dislocation in particular preventing dislocations i.e. warping of the M*N substrate material which is in turn a form of a Group III nitride compound (See Section 0040). This and the fact that the methodology of creating said substrate will have the rear surface of said silicon substrate opposing the surface on which said group III nitride compound semiconductor layer is formed.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to G. Nagesh Rao whose telephone number is (571) 272-2946. The examiner can normally be reached on 9AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Duane Smith can be reached on (571) 272-1166. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GNR

DUANE SMITH PRIMARY EXAMINER

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